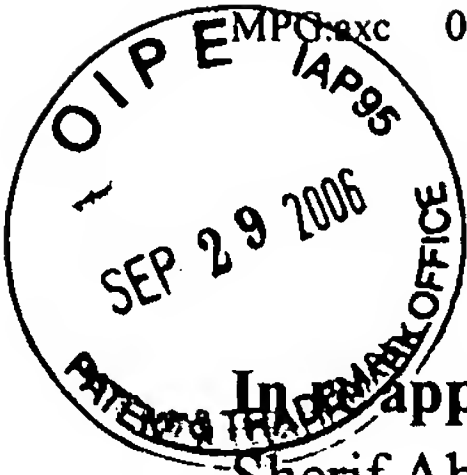


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PATENT



MPG:exc 09/25/06 7126-72254-01 3419/05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In application of:**

Sherif Ahmed Abdel-Wahab Hammouda et al.

**Application No.** 10/554,139

**Filed:** October 20, 2005

**Confirmation No.** Not yet assigned

**For:** ANALOG DESIGN RETARGETING

**Examiner:** Not yet assigned

**Art Unit:** Not yet assigned

**Attorney Reference No.** 7126-72254-01

**CERTIFICATE OF MAILING**

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

Attorney or Agent  
for Applicant(s)

*Michael P. Girard*  
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**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 C.F.R. § 1.97(b)**

Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Applicants filed this Information Disclosure Statement ("IDS") within three months of the filing date of a national application, within three months of the date of entry of the national stage as set forth in § 1.491 in an international application, before the mailing date of a first Office action on the merits, or before the mailing of a first Office action after the filing of request for

continued examination under § 1.114. As a result, no fee should be required to file this IDS.

However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

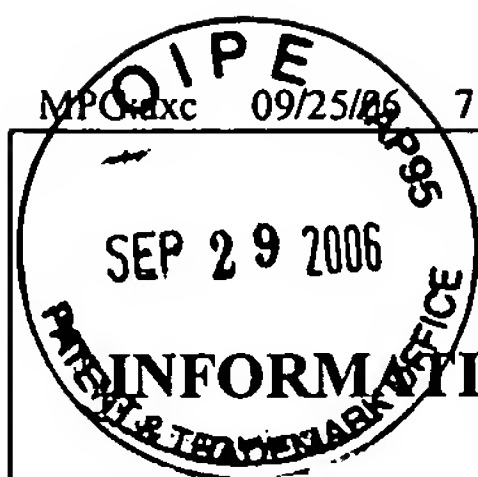
KLARQUIST SPARKMAN, LLP

By

  
Michael P. Girard  
Registration No. 38,467

One World Trade Center, Suite 1600  
121 S.W. Salmon Street  
Portland, Oregon 97204  
Telephone: (503) 595-5300  
Facsimile: (503) 595-5301

cc: Docketing



## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number	7126-72254-01
Application Number	10/554,139
Filing Date	October 20, 2005
First Named Inventor	Sherif Ahmed Abdel-Wahab Hammouda
Art Unit	Not yet assigned
Examiner Name	Not yet assigned

### U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee
		2005/028113 A1	February 3, 2005	Lin Yiqun et al.

### FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

### OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	
		Funaba et al., "A Fast and Accurate Method of Redesigning Analog Subcircuits for Technology Scaling," <i>1998 Design Automation Conference</i> , pp. 489-494 (February 10, 1998).
		Hammouda et al., "Analog IP Migration Using Design Knowledge Extraction," <i>IEEE 2004 Custom Integrated Circuits Conference</i> , pp. 333-336 (October 3, 2004).
		Phelps et al., "A Case Study of Synthesis for Industrial-Scale Analog IP: Redesign of the Equalizer/Filter Frontend for an ADSL Codec," <i>37<sup>th</sup> Annual Design Automation Conference</i> , pp. 1-6 (June 5, 2000).
		Xu Jinghan et al., "IC Design Automation from Circuit Level Optimization to Retargetable Layout," <i>Electronics, Circuits and Systems, ICECS 2001</i> , pp. 95-98 (September 2, 2001).
		International Search Report and Written Opinion for International Application No. PCT/EP2005/054928 (September 29, 2005).

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\* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.